

CHIP COIL (CHIP INDUCTORS) LQW18CN□□□□0ZD
Murata Standard Reference Specification 【AEC-Q200】

1. Scope

This reference specification applies to LQW18CN_0Z series, Chip coil (Chip Inductors) for automotive Electronics based on AEC-Q200 except for Power train and Safety.

2. Part Numbering

(ex)	LQ	W	18	C	N	4N9	D	0	Z	D
	Product ID	Structure	Dimension (L×W)	Applications and Characteristics	Category	Inductance	Tolerance	Features	Application Z:Automotive	Packaging D:Taping

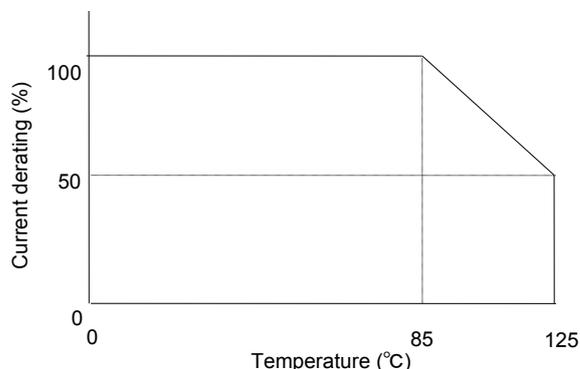
3. Rating

- Operating Temperature Range
 (Ambient temperature ; Self-temperature rise is not included) -40°C to +125°C (*)
- Storage Temperature Range. -40°C to +125°C

Customer Part Number	MURATA Part Number	Inductance		DC Resistance (Ω max.)	Self Resonant Frequency (MHz min.)	Rated Current (mA)	ESD Rank 5A:8kV
		(nH)	Tolerance				
	LQW18CN4N9D0ZD	4.9	D:±0.5nH	0.015	2300	2600	5A
	LQW18CN15NJ0ZD	15	J:±5%	0.025	2000	2200	
	LQW18CN33NJ0ZD	33		0.035	1800	1700	
	LQW18CN55NJ0ZD	55		0.045	1600	1500	
	LQW18CN85NJ0ZD	85		0.060	1380	1400	
	LQW18CNR10K0ZD	100	K:±10%	0.10	1260	1000	
	LQW18CNR12J0ZD	120	J:±5%	0.085	1200	1100	
	LQW18CNR16J0ZD	160		0.10	900	1000	
	LQW18CNR21J0ZD	210		0.15	720	800	
	LQW18CNR27J0ZD	270		0.16	660	750	
	LQW18CNR33J0ZD	330		0.25	600	630	
	LQW18CNR39J0ZD	390		0.28	570	620	
	LQW18CNR47J0ZD	470		0.45	555	500	
	LQW18CNR56J0ZD	560		0.48	540	450	
	LQW18CNR65J0ZD	650	0.52	510	430		

(*) As for LQW type, Rated Current is derated as following figure depending on the operating temperature.

Derating of Rated Current depend on Operating Temperature



4. Testing Conditions

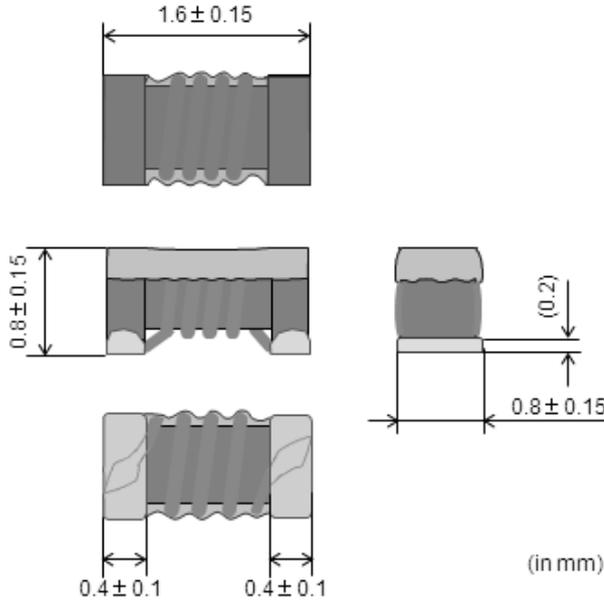
《Unless otherwise specified》

Temperature : Ordinary Temperature / 15°C to 35°C
 Humidity : Ordinary Humidity / 25%(RH) to 85%(RH)

《In case of doubt》

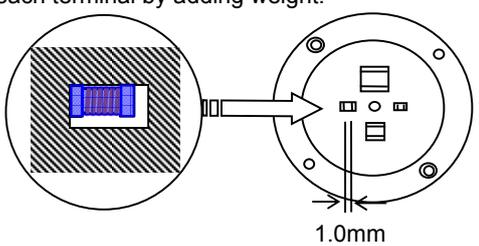
Temperature : 20°C±2°C
 Humidity : 60%(RH) to 70%(RH)
 Atmospheric Pressure : 86kPa to 106 kPa

5. Appearance and Dimensions



■ Unit Mass (Typical value)
 0.004g

6. Electrical Performance

No.	Item	Specification	Test Method
6.1	Inductance	Inductance shall meet item 3.	Measuring Equipment : KEYSIGHT 4991A or equivalent Measuring Frequency : <Inductance> 10MHz <SRF> Frequency when L value is 0μH Measuring Condition : Test signal level / about 0dBm Electrode spaces / 1.0mm Electrical length / 10mm Measuring Fixture : KEYSIGHT 16197A Position coil under test as shown in below and contact coil with each terminal by adding weight.
6.2	Self Resonant Frequency (S.R.F)		 <p style="text-align: center;">1.0mm</p> Measuring Method : See the endnote. <Electrical Performance : Measuring Method of Inductance>
6.3	DC Resistance	DC Resistance shall meet item 3.	Measuring Equipment : Digital multi meter
6.4	Rated Current	Self temperature rise shall be limited to 40°C max.	The rated current is applied.

7. Q200 Requirement

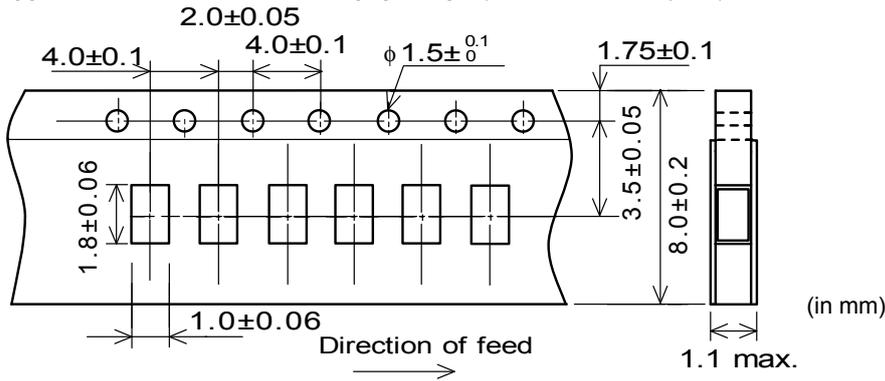
7.1.Performance (based on Table 5 for Magnetics(Inductors / Transformer)

AEC-Q200 Rev.D issued June 1. 2010

AEC-Q200			Murata Specification / Deviation				
No	Stress	Test Method					
3	High Temperature Exposure	1000hours at 125 deg C Set for 24hours at room temperature, then measured.	Meet Table A after testing. <u>Table A</u> <table border="1"> <tr> <td>Appearance</td> <td>No damage</td> </tr> <tr> <td>Inductance change</td> <td>Within ±5%</td> </tr> </table>	Appearance	No damage	Inductance change	Within ±5%
Appearance	No damage						
Inductance change	Within ±5%						
4	Temperature Cycling	1000cycles -40 deg C to +125 deg C Set for 24hours at room temperature, then measured.	Meet Table A after testing.				
7	Biased Humidity	1000hours at 85 deg C, 85%R.H. unpowered.	Meet Table A after testing.				
8	Operational Life	Apply 125 deg C 1000hours Set for 24hours at room temperature, then measured	Meet Table A after testing. Apply derating of rated current.				
9	External Visual	Visual inspection	No abnormalities				
10	Physical Dimension	Meet ITEM 5 (Style and Dimensions)	No defects				
12	Resistance to Solvents	Per MIL-STD-202 Method 215	Not Applicable				
13	Mechanical Shock	Per MIL-STD-202 Method 213 Condition C : 100g's (0.98N), 6ms, Half sine, 12.3ft / s	Meet Table A after testing.				
14	Vibration	5g's (0.049N) for 20 minutes, 12cycles each of 3 orientations Test from 10-2000Hz.	Meet Table A after testing.				
15	Resistance to Soldering Heat	No-heating Solder temperature 260C+/-5 deg C Immersion time 10s	Pre-heating : 150C + / -10 deg C, 60s to 90s 270C + / - 5 deg C Immersion time 5s Meet Table A after testing.				
17	ESD	Per AEC-Q200-002	ESD Rank : Refer to Item 3. Rating. Meet Table A after testing				
18	Solderability	Per J-STD-002	Method b : Not Applicable 95% of the terminations is to be soldered. (Except exposed wire)				
19	Electrical Characterization	Measured : Inductance	No defects				
20	Flammability	Per UL-94	Not Applicable				
21	Board Flex	Epoxy-PCB (1.6mm) Deflection 2mm (min) Holding time 60s	Meet Table A after testing.				
22	Terminal Strength	Per AEC-Q200-006 A force of 17.7N for 60s	Appearance No damage Murata Deviation Request : 8N / 5s				

8. Specification of Packaging

8.1 Appearance and Dimensions of paper tape (8mm-wide, 2mm pitch)



8.2 Specification of Taping

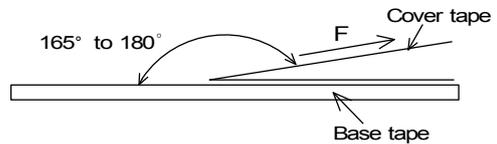
- (1) Packing quantity (standard quantity)
4,000 pcs. / reel
- (2) Packing Method
Products shall be packed in the cavity of the base tape and sealed by Cover tape.
- (3) Sprocket hole
The sprocket holes are to the right as the tape is pulled toward the user.
- (4) Spliced point
Base tape and Cover tape has no spliced point.
- (5) Missing components number
Missing components number within 0.1% of the number per reel or 1 pc., whichever is greater, and are not continuous. The Specified quantity per reel is kept.

8.3 Pull Strength

Cover tape	5N min.
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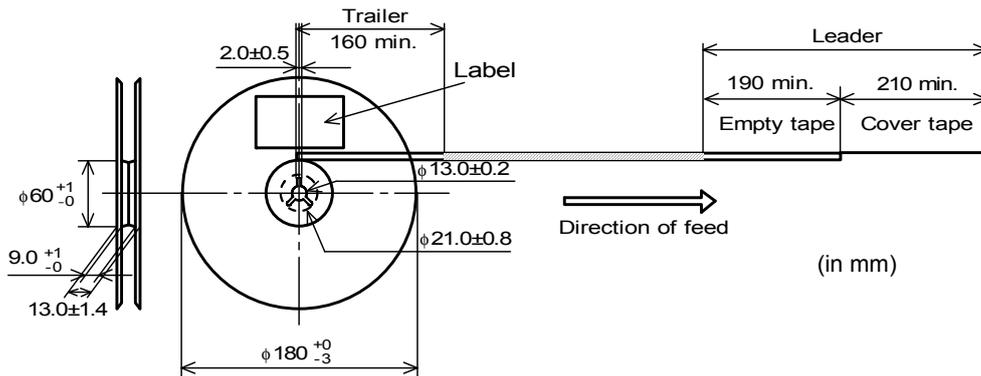
8.4 Peeling off force of cover tape

Speed of Peeling off	300mm/min
Peeling off force	0.1N to 0.6N (minimum value is typical)



8.5 Dimensions of Leader-tape, Trailer and Reel

There shall be leader-tape (cover tape and empty tape) and trailer-tape (empty tape) as follows.

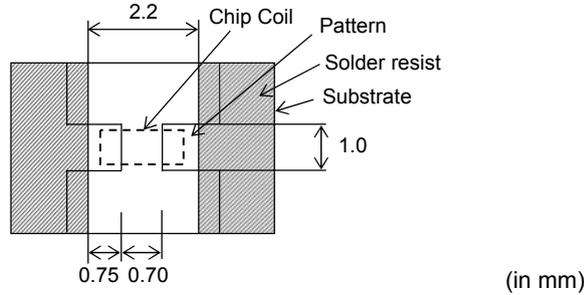


10. Notice

Products can only be soldered with reflow.
 This product is designed for solder mounting.
 Please consult us in advance for applying other mounting method such as conductive adhesive.

10.1 Land pattern designing

Recommended land patterns for reflow soldering are as follows :
 These have been designed for Electric characteristics and solderability.
 Please follow the recommended patterns. Otherwise, their performance which includes electrical performance or solderability may be affected, or result to "position shift" in soldering process.

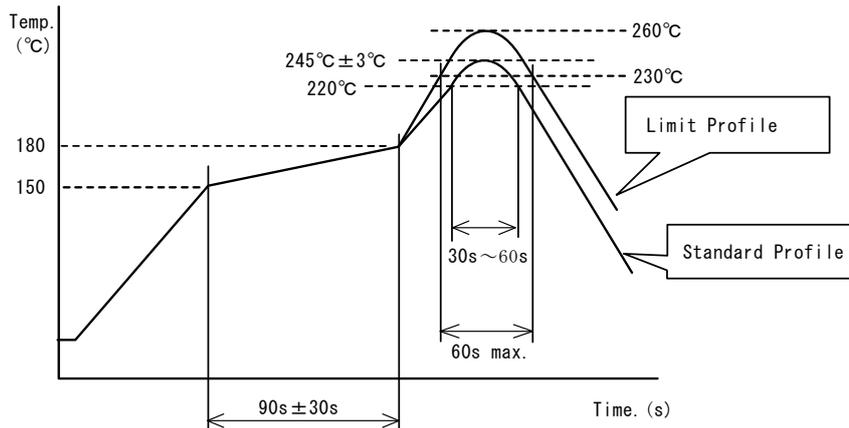


10.2 Flux, Solder

- Use rosin-based flux.
 Includes middle activator equivalent to 0.06(wt)% to 0.1(wt) % Chlorine.
 Don't use highly acidic flux with halide content exceeding 0.2(wt) % (chlorine conversion value).
 Don't use water-soluble flux.
- Use Sn-3.0Ag-0.5Cu solder.
- Standard thickness of solder paste : 50 μ m to 100 μ m.
- Please pay attention to solder paste's penetrating in order to avoid short circuit between the lines.

10.3 Reflow soldering conditions

- Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 100°C max. Cooling into solvent after soldering also should be in such a way that the temperature difference is limited to 100°C max.
 Insufficient pre-heating may cause cracks on the product, resulting in the deterioration of products quality.
- Standard soldering profile and the limit soldering profile is as follows.
 The excessive limit soldering conditions may cause leaching of the electrode and / or resulting in the deterioration of product quality.
- Reflow soldering profile



	Standard Profile	Limit Profile
Pre-heating	150°C~180°C , 90s±30s	
Heating	above 220°C, 30s~60s	above 230°C, 60s max.
Peak temperature	245°C±3°C	260°C,10s
Cycle of reflow	2 times	

10.4 Reworking with soldering iron

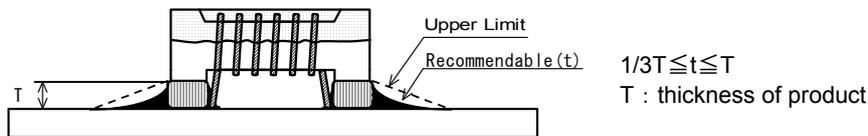
The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150°C, 1 min
Tip temperature	350°C max.
Soldering iron output	80W max.
Tip diameter	φ3mm max.
Soldering time	3 (+1,-0)s
Time	2 times

Note : Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the products due to the thermal shock.

10.5 Solder Volume

- Solder shall be used not to be exceeded the upper limits as shown below.
- Accordingly increasing the solder volume, the mechanical stress to Chip is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance.

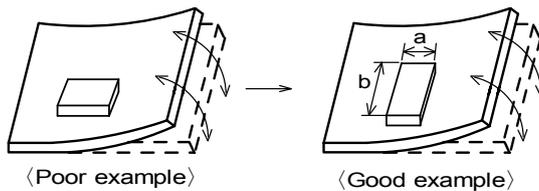


10.6 Product's location

The following shall be considered when designing and laying out P.C.B.'s.

- (1) P.C.B. shall be designed so that products are not subject to the mechanical stress due to warping the board.

[Products direction]



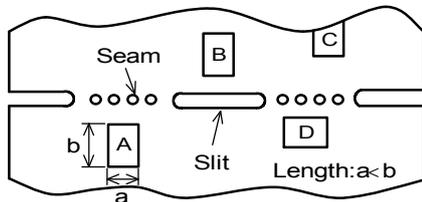
Products shall be located in the sideways direction (Length : $a < b$) to the mechanical stress.

- (2) Components location on P.C.B. separation.

It is effective to implement the following measures, to reduce stress in separating the board.

It is best to implement all of the following three measures; however, implement as many measures as possible to reduce stress.

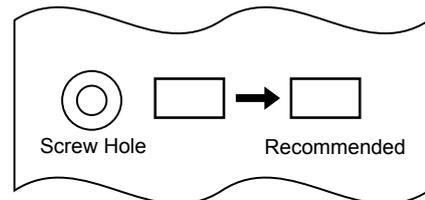
Contents of Measures	Stress Level
(1) Turn the mounting direction of the component parallel to the board separation surface.	$A > D^{*1}$
(2) Add slits in the board separation part.	$A > B$
(3) Keep the mounting position of the component away from the board separation surface.	$A > C$



*1 $A > D$ is valid when stress is added vertically to the perforation as with Hand Separation. If a Cutting Disc is used, stress will be diagonal to the PCB, therefore $A > D$ is invalid.

- (3) Mounting Components Near Screw Holes

When a component is mounted near a screw hole, it may be affected by the board deflection that occurs during the tightening of the screw. Mount the component in a position as far away from the screw holes as possible.



10.7 Cleaning Conditions

Products shall be cleaned on the following conditions.

- (1) Cleaning temperature shall be limited to 60°C max.(40°C max for IPA)
- (2) Ultrasonic cleaning shall comply with the following conditions with avoiding the resonance phenomenon at the mounted products and P.C.B.
Power : 20 W / l max. Frequency : 28kHz to 40kHz Time : 5 min max.
- (3) Cleaner
 1. Alcohol type cleaner
Isopropyl alcohol (IPA)
 2. Aqueous agent
PINE ALPHA ST-100S
- (4) There shall be no residual flux and residual cleaner after cleaning.
In the case of using aqueous agent, products shall be dried completely after rinse with de-ionized water in order to remove the cleaner.
- (5) Other cleaning Please contact us.

10.8 Resin coating

The inductance value may change due to high cure-stress of resin to be used for coating/molding products.

An open circuit issue may occur by mechanical stress caused by the resin, amount/cured shape of resin, or operating condition etc. Some resin contains some impurities or chloride possible to generate chlorine by hydrolysis under some operating condition may cause corrosion of wire of coil, leading to open circuit. So, please pay your careful attention when you select resin in case of coating/molding the products with the resin.

Prior to use the coating resin, please make sure no reliability issue is observed by evaluating products mounted on your board.

10.9 Caution for use

- Sharp material such as a pair of tweezers or other material such as bristles of cleaning brush , shall not be touched to the winding portion to prevent the breaking of wire.
- Mechanical shock should not be applied to the products mounted on the board to prevent the breaking of the core.

10.10 Notice of product handling at mounting

In some mounting machines,when picking up components support pin pushes up the components from the bottom of base tape. In this case, please remove the support pin. The support pin may damage the components and break wire.

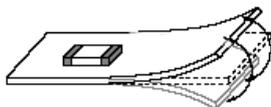
In rare case ,the laser recognition can not recognize this component. Please contact us when you use laser recognition. (There is no problem with the permeation and reflection type.)

10.11 Handling of a substrate

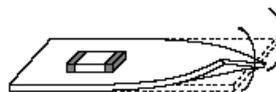
After mounting products on a substrate, do not apply any stress to the product caused by bending or twisting to the substrate when cropping the substrate, inserting and removing a connector from the substrate or tightening screw to the substrate.

Excessive mechanical stress may cause cracking in the product.

Bending



Twisting

**10.12 Storage and Handling Requirements****(1) Storage period**

Use the products within 12 months after delivered.

Solderability should be checked if this period is exceeded.

(2) Storage conditions

- Products should be stored in the warehouse on the following conditions.

Temperature : -10°C to 40°C

Humidity : 15% to 85% relative humidity No rapid change on temperature and humidity

- Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.
- Products should not be stored on bulk packaging condition to prevent the chipping of the core and the breaking of winding wire caused by the collision between the products.
- Products should be stored on the palette for the prevention of the influence from humidity, dust and so on.
- Products should be stored in the warehouse without heat shock, vibration, direct sunlight and so on.

(3) Handling Condition

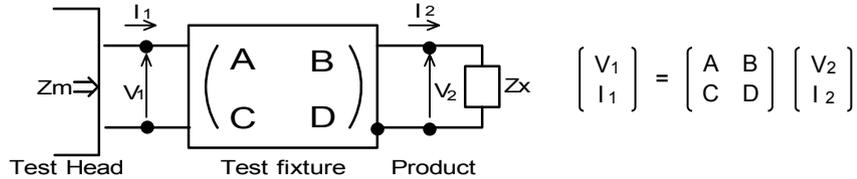
Care should be taken when transporting or handling product to avoid excessive vibration or mechanical shock.

11. ⚠ Note

- (1) Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- (2) You are requested not to use our product deviating from the reference specifications.
- (3) The contents of this reference specification are subject to change without advance notice.
Please approve our product specifications or transact the approval sheet for product specifications before ordering.

< Electrical Performance:Measuring Method of Inductance/Q >

(1) Residual elements and stray elements of test fixture can be described by F-parameter shown in following.



(2) The impedance of chip coil Z_x and measured value Z_m can be described by input/output current/voltage.

$$Z_m = \frac{V_1}{I_1} \quad , \quad Z_x = \frac{V_2}{I_2}$$

(3) Thus, the relation between Z_x and Z_m is following;

$$Z_x = \alpha \frac{Z_m - \beta}{1 - Z_m \Gamma} \quad \text{where, } \alpha = D / A = 1$$

$$\beta = B / D = Z_{sm} - (1 - Y_{om}) Z_{ss}$$

$$\Gamma = C / A = Y_{om}$$

- Z_{sm} : measured impedance of short chip
- Z_{ss} : residual impedance of short chip (0.771nH)
- Y_{om} : measured admittance when opening the fixture

(4) L_x shall be calculated with the following equation.

$$L_x = \frac{\text{Im}(Z_x)}{2\pi f}$$

L_x : Inductance of chip coil
 f : Measuring frequency